

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Larry E. Mosley

U.S. Serial No: 10/613,839

Examiner: Nguyen, Thinh T.

Filed: July 3, 2003

Art Unit: 2818

For: METHOD OF FORMING A LOW
INDUCTANCE HIGH CAPACITANCE
CAPACITOR

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

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The assignee of the entire right, title, and interest in and to the above-referenced patent application is Intel Corporation ("assignee"),
(Name of Assignee)

a Delaware corporation having a place of business at
(State of Incorporation)

2200 Mission College Boulevard, Santa Clara, California 95050.
(Address)

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The terminal part of any patent granted on the above-identified application that would extend beyond the expiration of the full statutory term of

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CAPACITANCE CAPACITOR, and dated
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No disclaimer is being made as to any terminal part of any patent granted on
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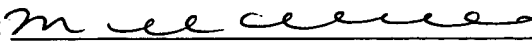
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Respectfully submitted,

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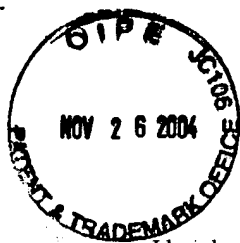
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